

11

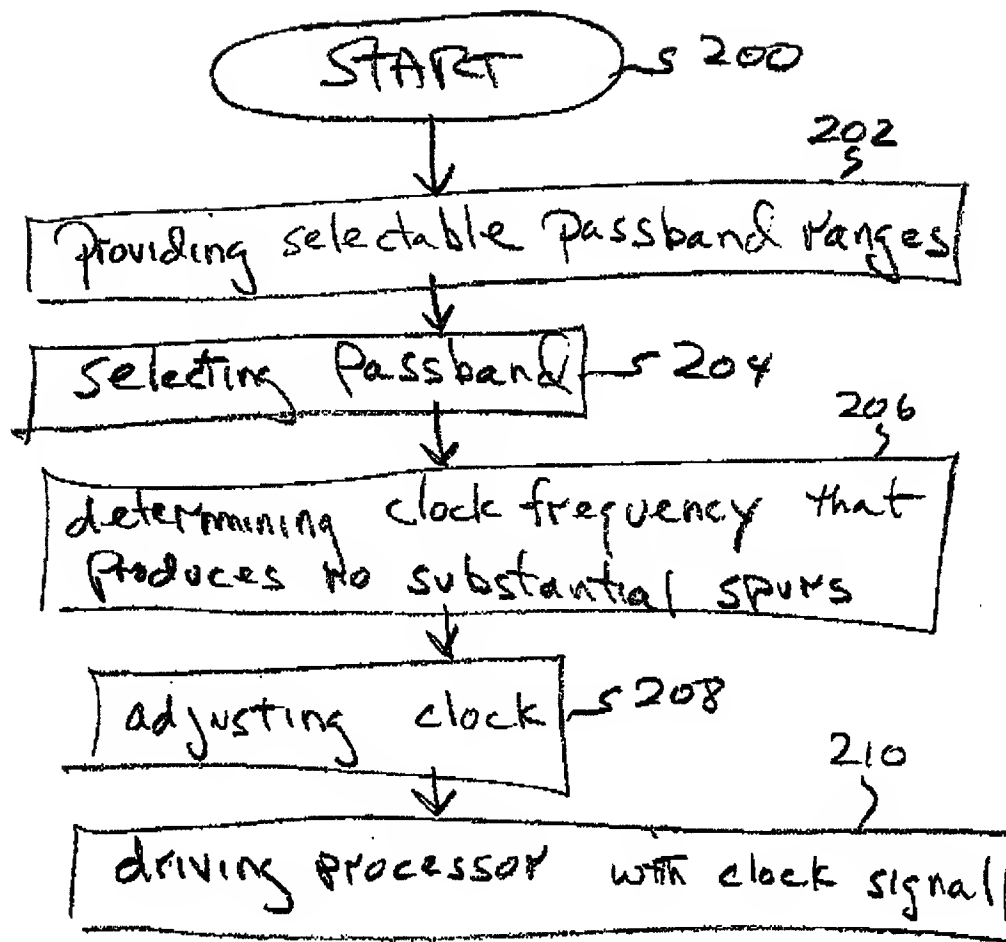


Fig. 2

START 300

Initially generating 19.2 MHz clock
frequency with a harmonic at 883.2 MHz 301

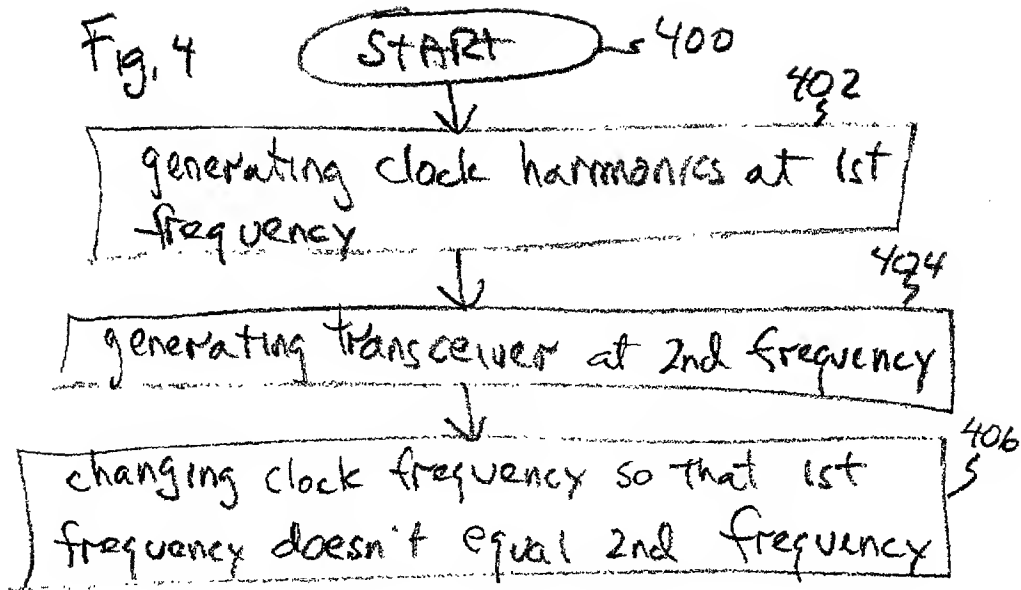
generating clock frequency with harmonics
at 1st frequency 302

generating transceiver carrier at 2nd
frequency 304

Selecting clock frequency so that
1st frequency does not equal
2nd frequency 306

Fig. 3

Fig. 4



START 500

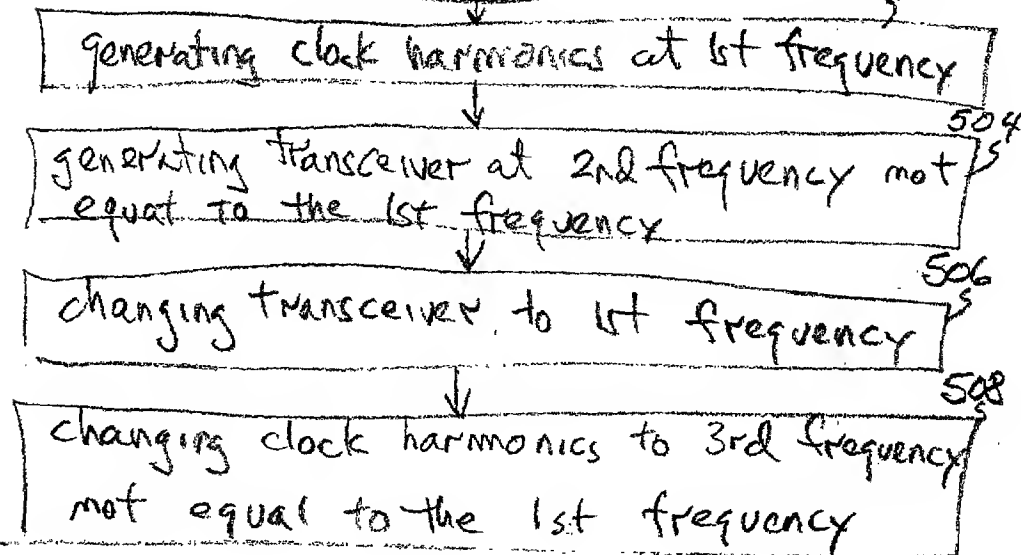


Fig. 5